

Figure 2 is a front view of the device of this invention.

Figure 3a is a circuit diagram for the anti-noise system of this invention.

Figure 3b is a schematic representation of the anti-noise system of Figure 3a.

Figure 4 is a circuit diagram of the radio receiving circuit of this invention.

Figure 5 is a circuit diagram of the sound-collection circuit system of this invention.

Figure 6 is a synthesis circuit system of the components of Figures 3a, 4 and 5.--.

Page 3, line 27, after "invention" insert --(See Figure 3a and 3b)---.

Same page, last line, after "(301)" insert --(See Figure 5)--.

Page 4, line 1, after "system" insert --(See Figure 4)--.

After page 5 insert the following Abstract: